



Intel Singapore Limited
Block 26 Ayer Rajah Crescent
No. 03-01 to 03-10
Ayer Rajah Industrial Estate
Singapore 0513

Dear Customer:

Thank you for buying this Intel iSBC* 80/24A single board computer. The iSBC 80/24A is an upgrade of the iSBC 80/24, an 8-bit CPU board Intel has been manufacturing since 1980.

The significant feature of the upgrade is the RAM memory portion of the board. The 4 KBytes of 8185-2 SRAM used on the iSBC 80/24 have been replaced with **8 KBytes of SRAM on board**. In order to get this 8 KBytes before the upgrade required a two-slot solution using the iSBC 80/24 and iSBC 301. The iSBC 80/24A is an 8 KByte, one slot solution; no memory expansion Multimodule is needed.

Also, the iSBC 80/24A is completely software and hardware compatible with the iSBC 80/24. The iSBC 80/24A upgrade is transparent to the 80/24 simplifying your transition from the old to the new.

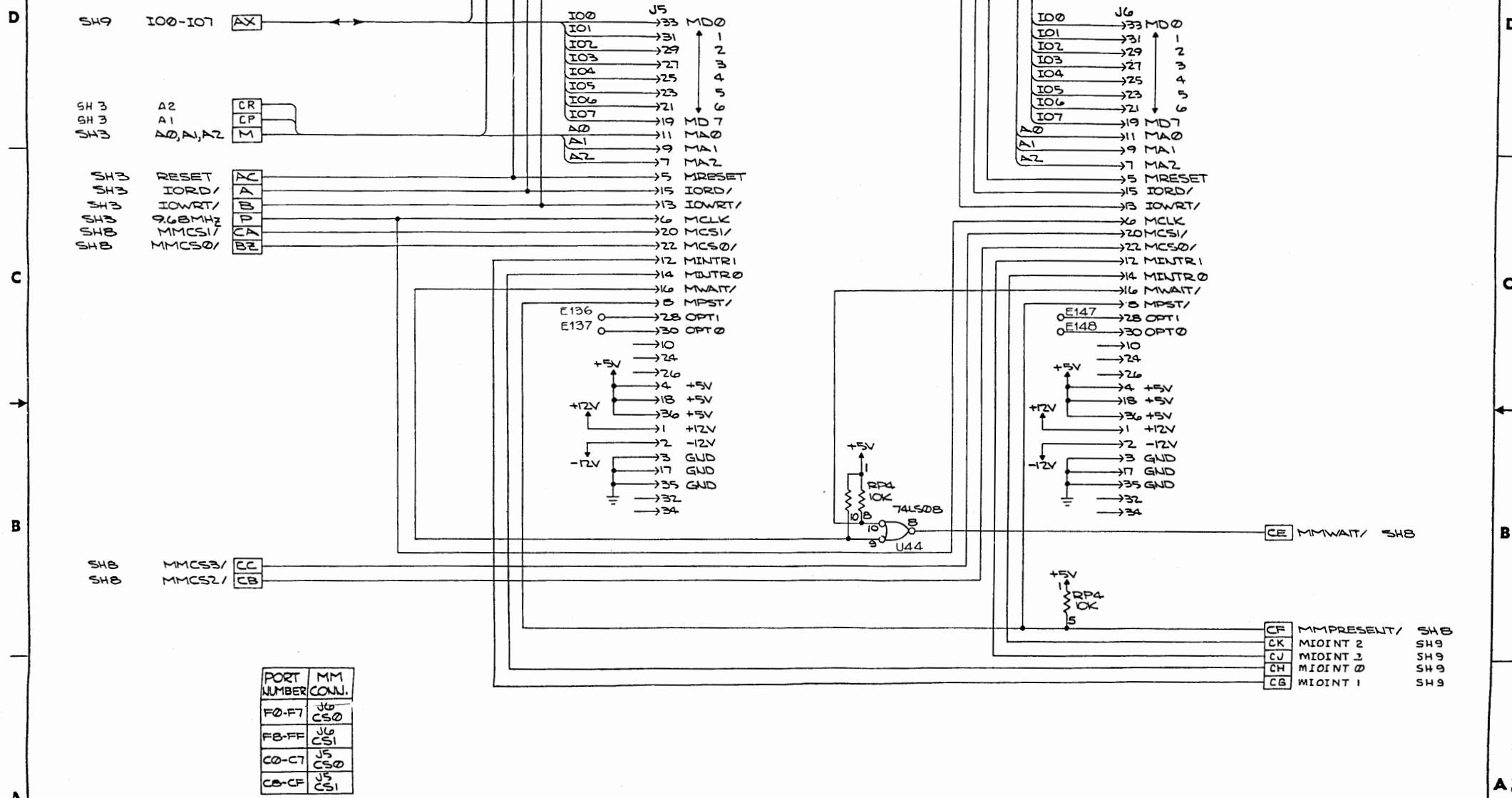
Intel offers a hardware subscription service called iMBX MULTIBUS* EXCHANGE. This service provides subscribers with regular publications of engineering changes, product change histories and helpful ancillary notes. If you are using the iSBC 80/24A board in a production product, we recommend that you consider this service as a means to stay informed of subtle changes and their potential implications.

For further information regarding Intel's iMBX Hardware Subscription Service, or if you have any questions regarding the iSBC 80/24A board, please contact your local Intel Sales Representative. Data Sheets and Hardware Reference Manuals for the iSBC 80/24A are available through Intel Literature and your Intel Sales Office.

* iSBC and MULTIBUS are trademarks of Intel Corporation.

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

ZONE	REV	DESCRIPTION	REVISONS	DFT	CHK	DATE	APPROVED
		SEE SHEET ONE					



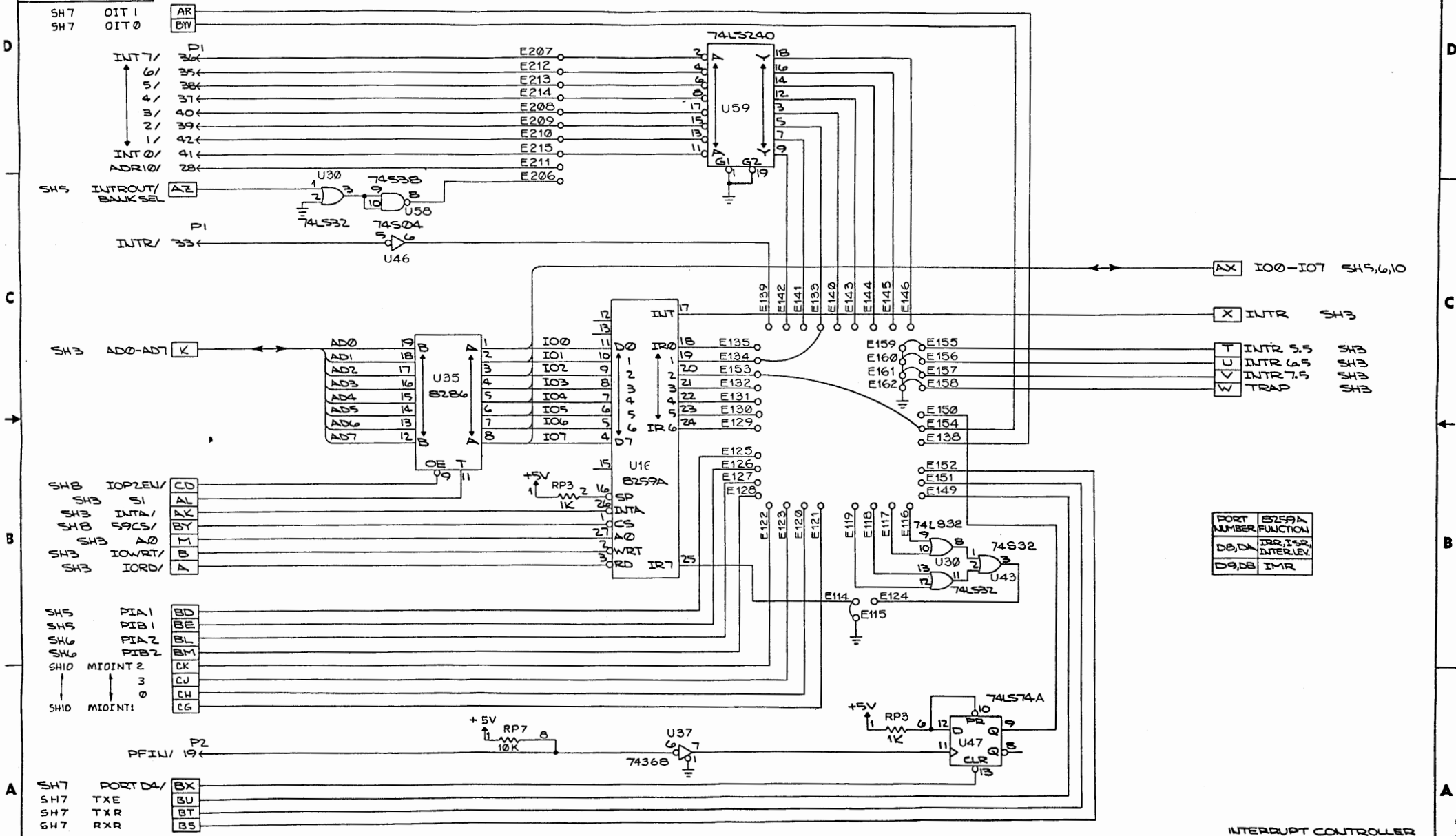
PORT NUMBER	MM CONN.
F0-F7	J6 CS0
F8-FE	J6 CS1
C0-C7	J5 CS0
C8-CF	J5 CS1

MULTIMODULE INTERFACES

SIZE	CODE	DWG NO.	REV
D		502032	A
ISSUED	SCALE	NONE	SHEET 10

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS REPRODUCED IN CONFIDENCE, AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

ZONE		REV	DESCRIPTION	DFT	CHK	DATE	APPROVED
			SEE SHEET ONE				



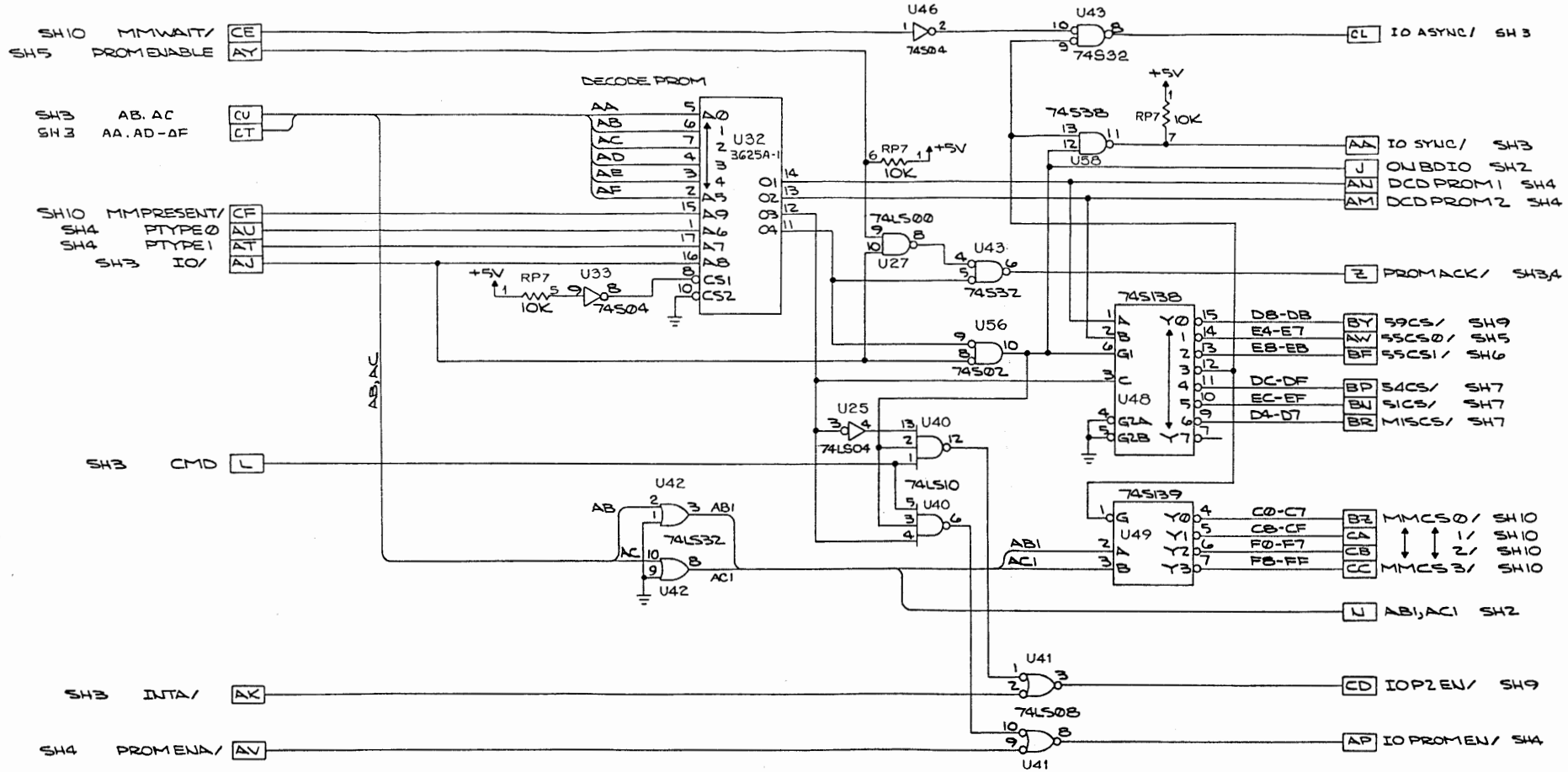
PORT NUMBER	74LS279 FUNCTION
D0, D1	INT, IAS, INTERLEV.
D9, D8	IMR

INTERRUPT CONTROLLER

DRAWN	SIZE D	CODE	DWG NO. 502032	REV A
ISSUED	SCALE	NOTE	SHEET 9	

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

ZONE REV		DESCRIPTION	DPT	CHK	DATE	APPROVED
		SEE SHEET ONE				

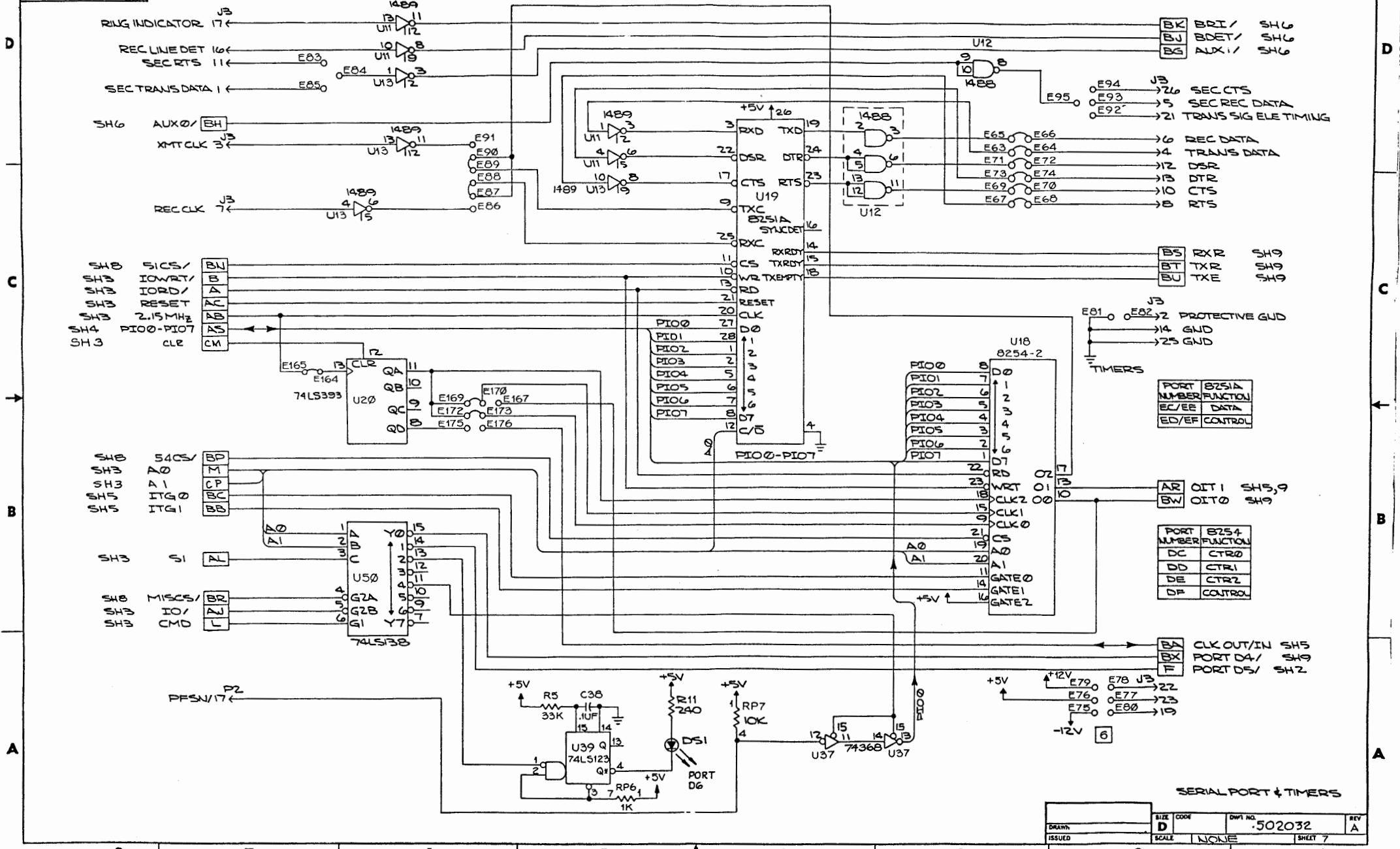


DECODE PROM # CHIP SELECT DECODE

DRAWN	ISSUED	SCALE	NOISE	SIZE	CODE	DWG NO.	REV
				D		502032	A
				SHEET 8			

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

ZONE		REV	DESCRIPTION	REVISIONS			
DFT	CHK	DATE	APPROVED				
SEE SHEET ONE							



PORT NUMBER	8251A FUNCTION
EC/EE	DATA
ED/EF	CONTROL

PORT NUMBER	8254 FUNCTION
DC	CTR0
DD	CTR1
DE	CTR2
DF	CONTROL

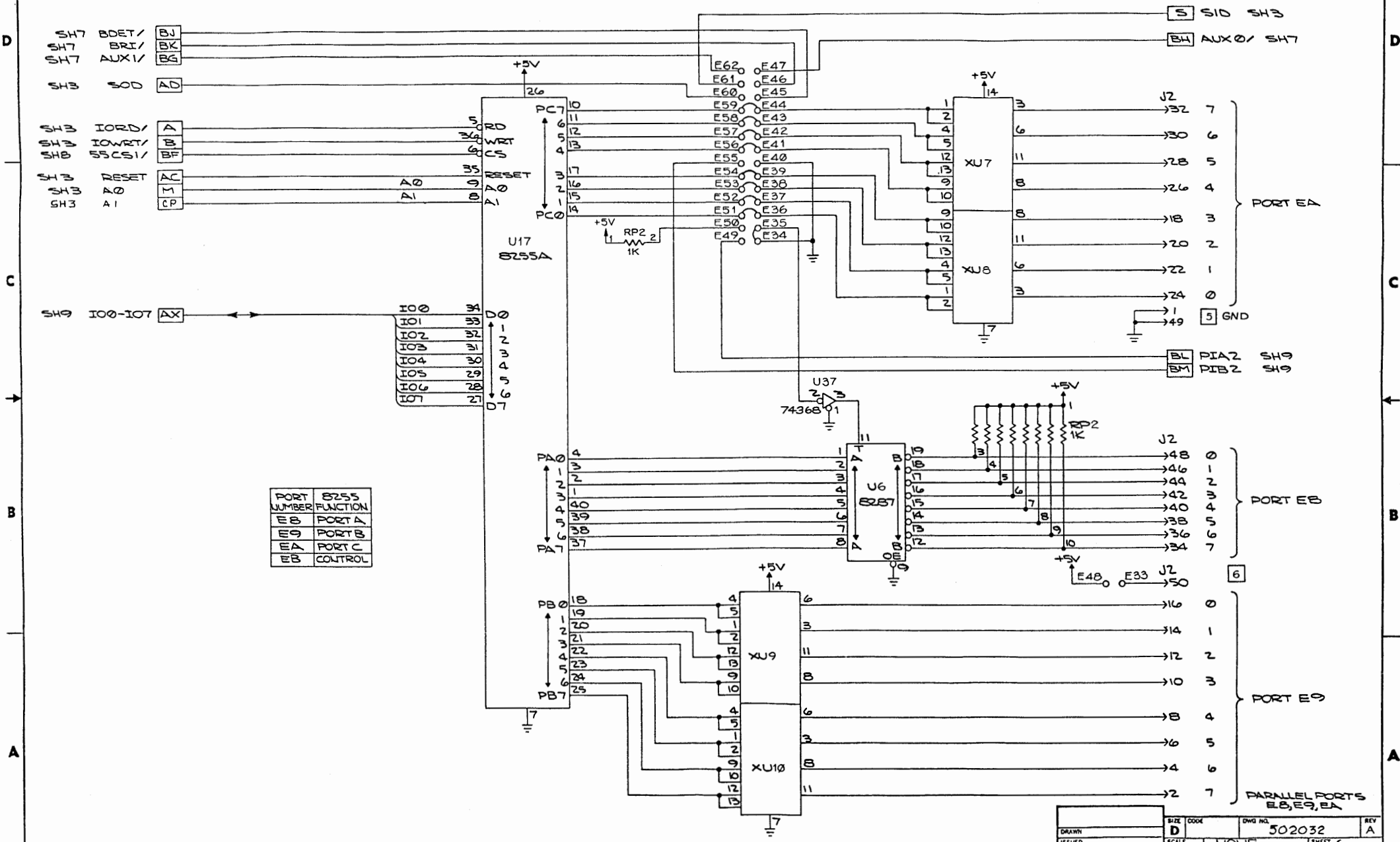
BA	CLK OUT/IN SH5
BX	PORT D4/ SH9
F	PORT D5/ SH2

SERIAL PORT & TIMERS

SIZE	CODE	DWG NO.	REV
D		.502032	A
SCALE	NONE	SHEET	7

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

DWG NO.		SH	REV
ZONE	REV	DESCRIPTION	REV
		SEE SHEET ONE	



PORT NUMBER	8255 FUNCTION
EA	PORT A
EB	PORT B
EC	PORT C
ED	CONTROL

PARALLEL PORTS
EB, EC, EA

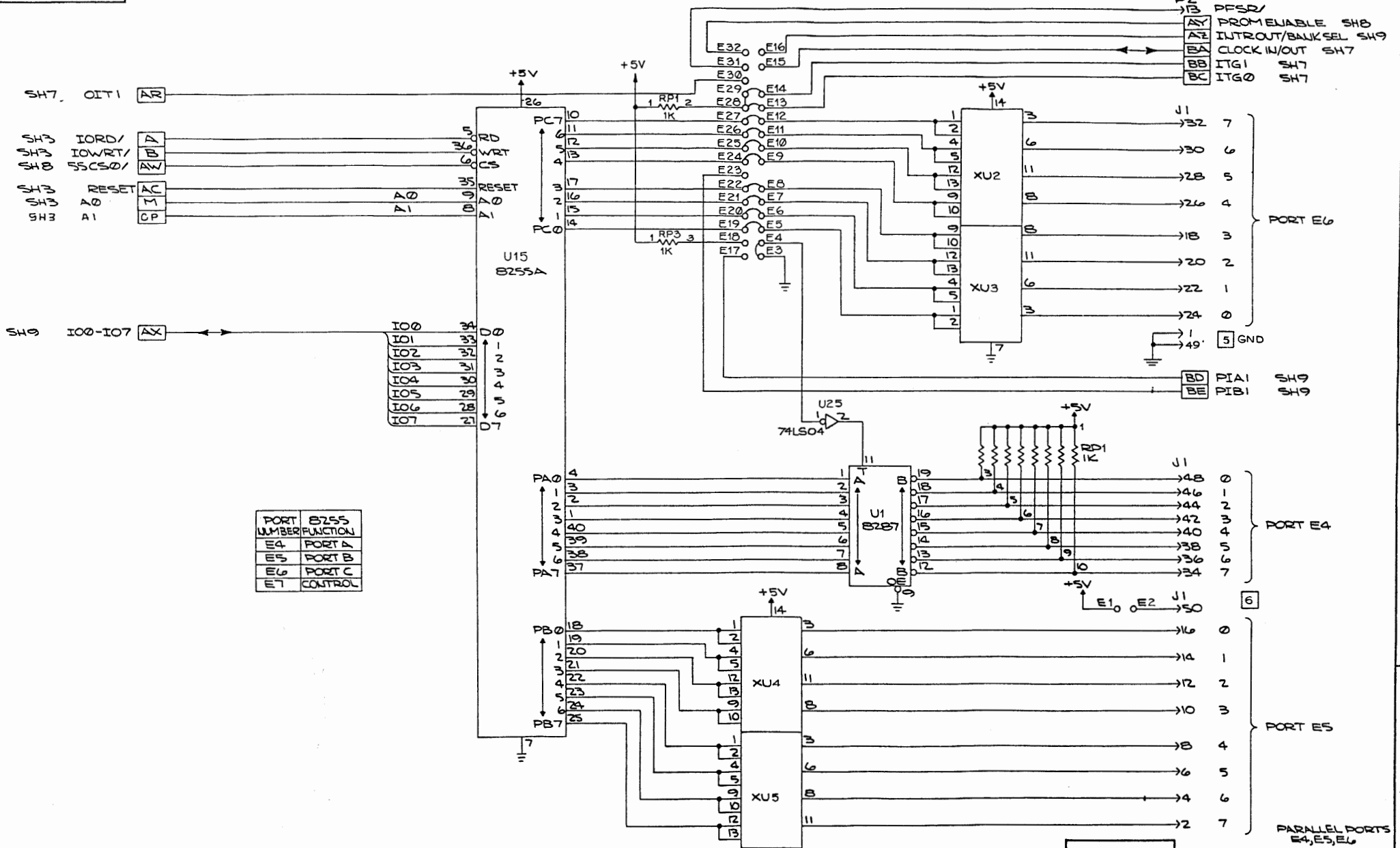
DRAWN	ISSUED	SIZE	CODE	DWG NO.	REV
		D		502032	A
SCALE			1/4" = 1"	SHEET	6

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

ZONE		REV		REVISIONS			
DESCRIPTION	DFT	CHK	DATE	APPROVED			
SEE SHEET ONE							

D
C
B
A

D
C
B
A



PORT NUMBER	8255 FUNCTION
E4	PORT A
E5	PORT B
E6	PORT C
E7	CONTROL

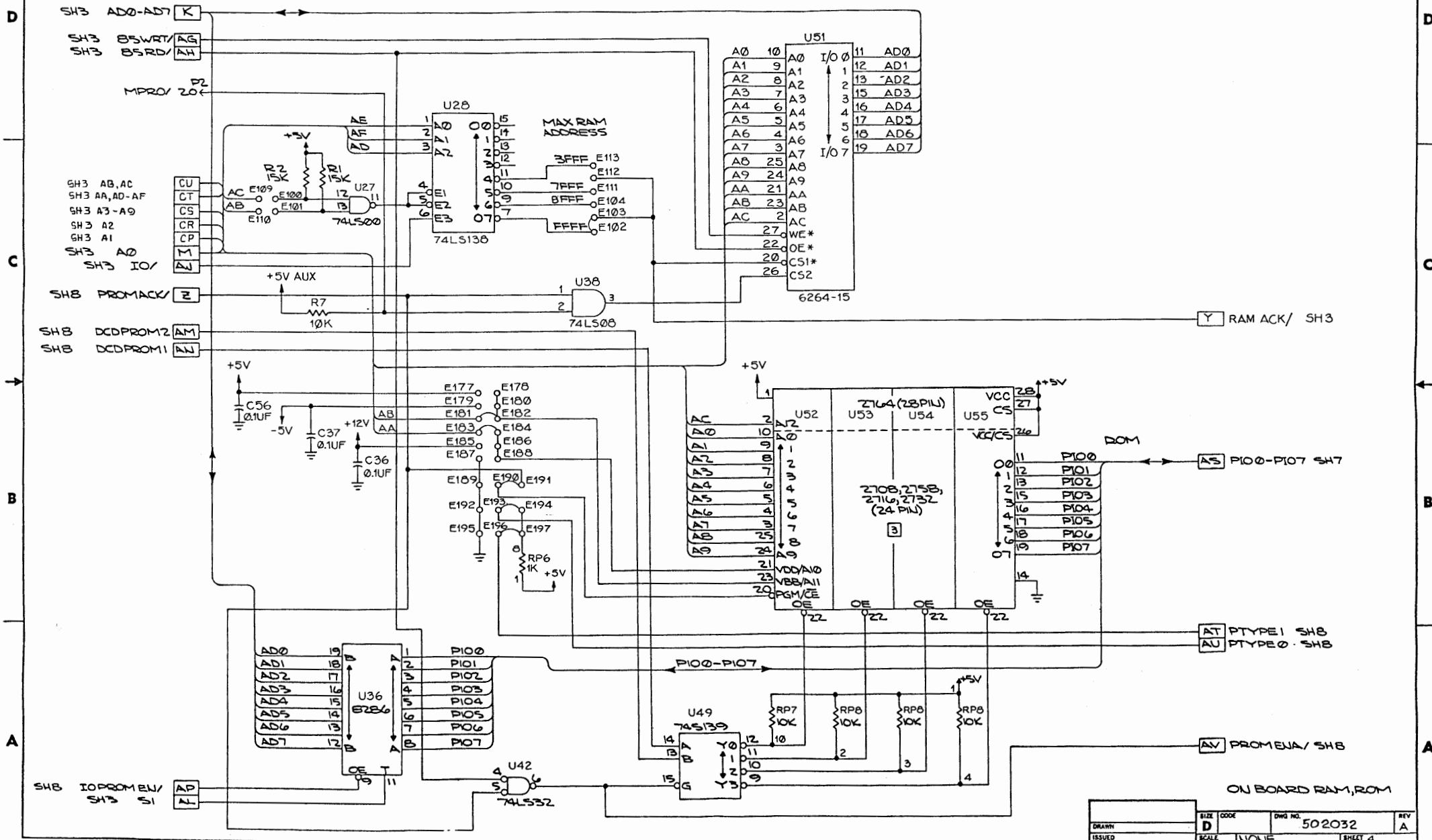
DRAWN	ISSUED	SCALE	WOLF	SIZE CODE	D	DWG NO.	502032	REV	A
						SHEET 5			

PARALLEL PORTS E4, E5, E6

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RELEASED IN CONFIDENCE, AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

MEM JUMPERS			
SIZE	AD	AC	
2K	IN	IN	
4K	WT	WT	
8K	OUT	OUT	

ZONE		REV	DESCRIPTION	DFT	CHK	DATE	APPROVED
			SEE SHEET ONE				



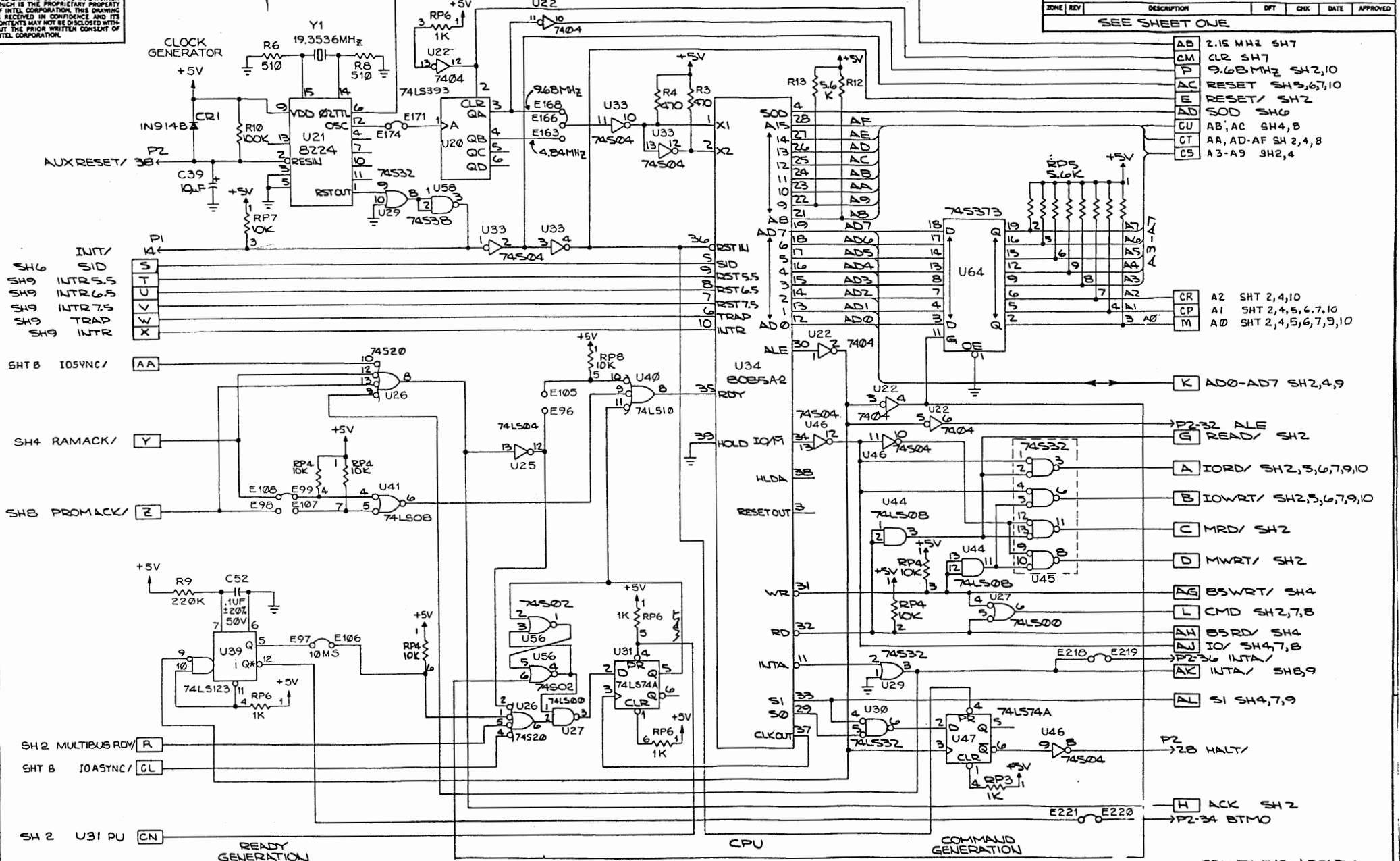
ON BOARD RAM, ROM

DRWN	SIZE	CODE	DWG NO.	REV
	D		502032	A
ISSUED	SCALE	NONE	SHEET 4	

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

DWG NO.	34	REV	REV
ZONE	REV	DESCRIPTION	DFT
SEE SHEET ONE			
DATE	APPROVED	CHK	REV

D
C
B
A



AB	2.15 MHz SH7
CM	CLR SH7
P	9.6MHz SH2,10
AC	RESET SH3,6,7,10
E	RESET/ SH2
AD	SOD SH6
CU	AB, AC SH4,8
CT	AA, AD-AF SH2,4,8
CS	A3-A9 SH2,4
CR	A2 SHT 2,4,10
CP	A1 SHT 2,4,5,6,7,10
M	A0 SHT 2,4,5,6,7,9,10

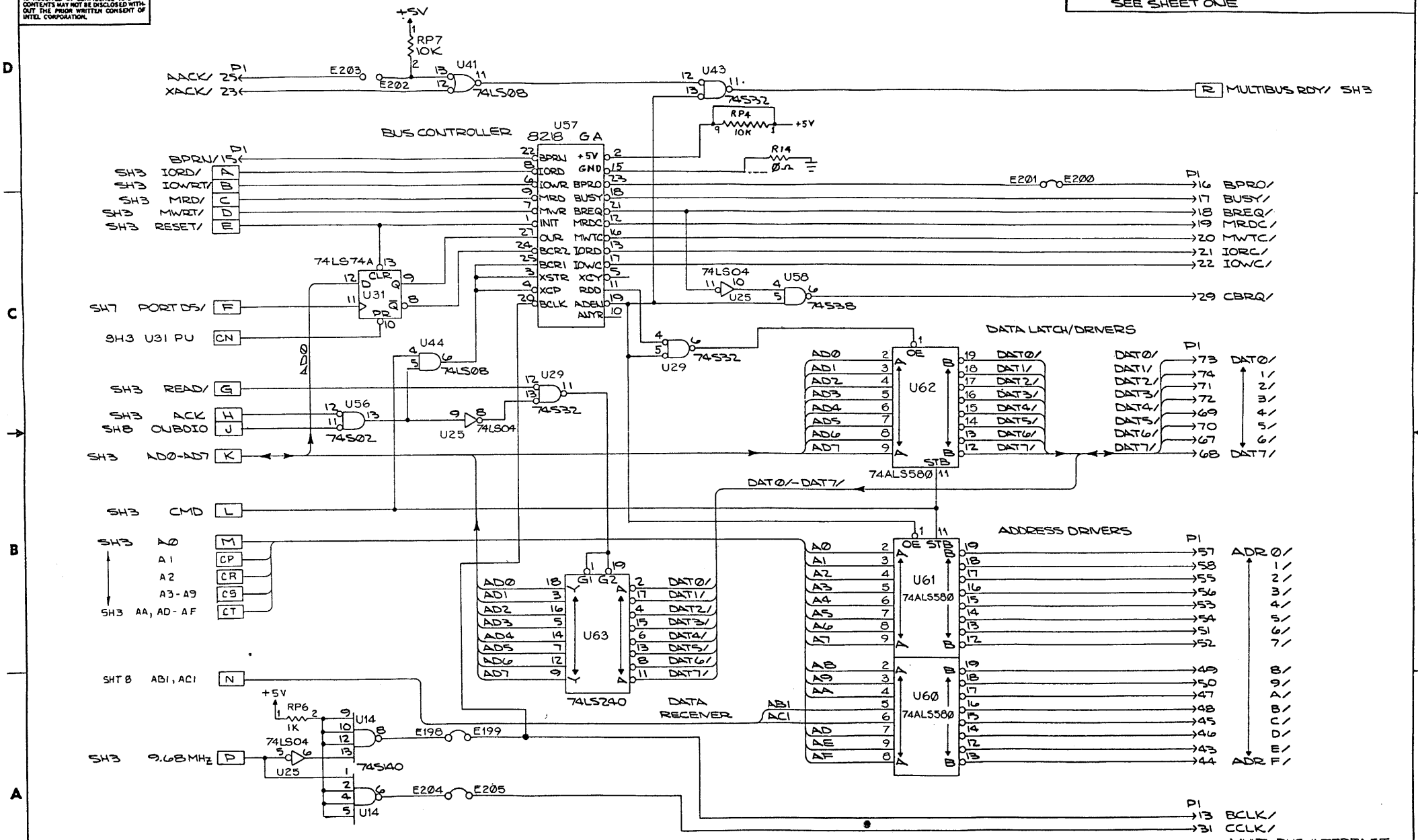
K	ADD-AD7 SH2,4,9
G	P2-32 ALE SH2
A	IORD/ SH2,5,6,7,9,10
B	IOWRT/ SH2,5,6,7,9,10
C	MRD/ SH2
D	MWRT/ SH2
AG	BSWRT/ SH4
L	CMD SH2,7,8
AH	BSRDV SH4
AL	IO/ SH4,7,8
AK	INTA/ SH8,9
N	SI SH4,7,9
H	ACK SH2
P2-34	BTMO

DRAWN	ISSUED	SIZE	CODE	DWG NO.	REV
		D		502032	A

CPU, TIMING, & READY

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

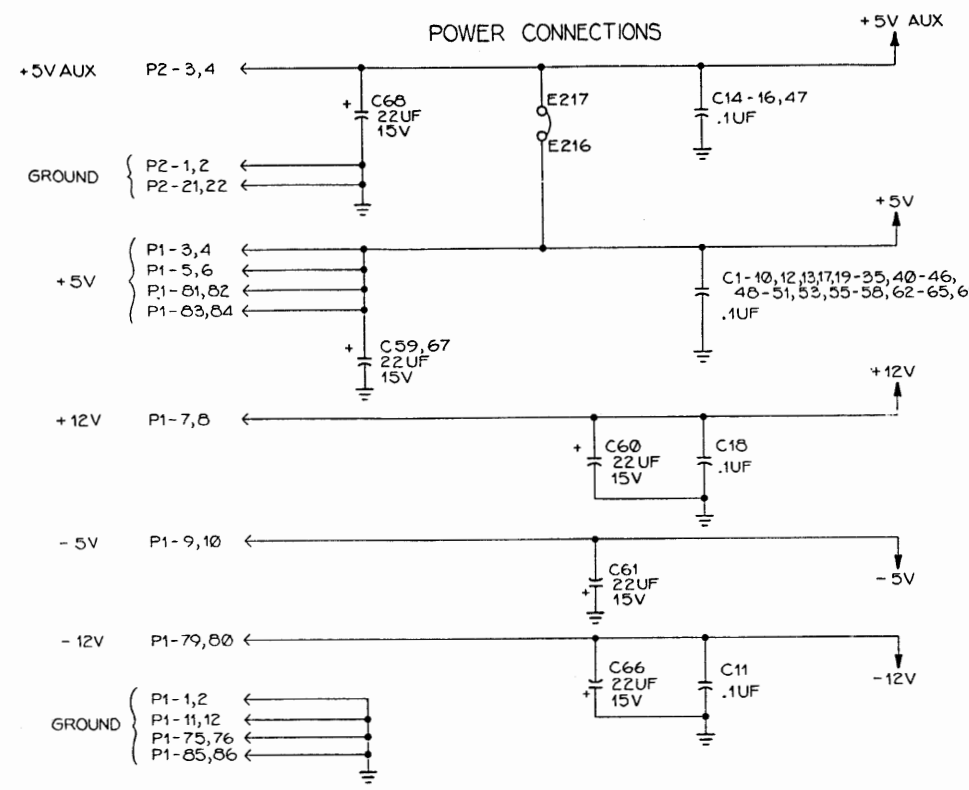
ZONE		REV	DESCRIPTION	DFT	CHK	DATE	APPROVED
			SEE SHEET ONE				



DRAWN	ISSUED	SIZE	CODE	DWG NO.	REV
		D		502032	A
		SCALE	UONE	SHEET 2	

REV	DESCRIPTION	DFT	REVISIONS				DATE	APVD	DATE
			DATE	CHK	DATE	APVD			
A	ECO 0500328	EA	4/17/79	AW	4/17/79	K	4/17/79		

POWER CONNECTIONS



- NOTES; UNLESS OTHERWISE SPECIFIED:
1. RESISTOR VALUES ARE IN OHMS, 1/4W, 5%.
 2. CAPACITOR VALUES ARE IN MICRO FARADS, +80%, -20%, 50V.
 3. ALL 24 PIN PARTS USED IN SOCKETS XU52 THRU XU55 SHOULD BE INSERTED WITH PIN 1 OF THE 24 PIN I.C. IN PIN 3 OF THE 28 PIN SOCKET.
 4. U2 THRU U5, U7 THRU U10, U52-U55 ARE SHOWN FOR CLARITY. ACTUAL COMPONENTS ARE CUSTOMER INSTALLED.
 5. ODD PINS ON J1 AND J2 ARE ALL GROUNDED.
 6. IMPROPERLY INSTALLED CONNECTORS (J1, J2 AND J3) COULD RESULT IN DAMAGE WHEN JUMPERS E2-E1, E33-E48, E76-E77, E78-E79, OR E80-E75 ARE INSTALLED.

DEVICE TABLE

REFERENCE DESIGNATION	DEVICE TYPE	POWER PINS					UNUSED LOGIC ELEMENT OUTPUT PINS
		GND	+5V	+12V	-5V	-12V	
U27	74LS00	7	14				
U56	74S02	7	14				
U22	7404	7	14				U22-8
U33,46	74S04	7	14				U33-6, U46-4
U25	74LS04	7	14				
U38,41,44	74LS08	7	14				U38-6,8,11
U40	74LS10	7	14				
U26	74S20	7	14				
U29,43,45	74S32	7	14				
U30,42	74LS32	7	14				U42-11
U58	74S38	7	14				
U31,47	74LS74A	7	14				
U39	74LS123	8	16				
U48	74S138	8	16				
U28,50	74LS138	8	16				
U49	74S139	8	16				
U14	74S140	7	14				
U59,63	74LS240	10	20				
U37	74368	8	16				U37-5,9
U64	74S373	10	20				
U20	74LS393	7	14				
U60-62	74ALS580	10	20				
U12	1488	7	14	14		1	
U11,13	1489	7	14				
U57	8218	14	28				
U32	3625A-1	9	18				
U51	6264-15	14					28
U34	8085A-2	20	40				
U21	8224	8	16				
U19	8251A	4	26				
U18	8254-2	12	24				
U15,17	8255A	7	26				
U16	8259A	14	28				
U35,36	8286	10	20				
U1,6	8287	10	20				
XU2-5	SOCKET	7	14				
XU7-10	SOCKET	7	14				
XU52-55	SOCKET	14	28				

INTEL PROPRIETARY INFORMATION

REF. DESIGNATION	SIGNATURE	DATE	intel®	3085 BOWERS AVE. SANTA CLARA CALIF. 95051
LAST USED	DRN BY E. ALONZO	5/25/80		
NOT USED	CHK BY E. ALONZO	5/25/80	TITLE	
U64, Y1	ENGR T. K.	5/15/80	SCH. PBA, SBC 80/24A	
RP8, DS1	APVD		SIZE	SCALE
R14, J6			CL CODE	DOCUMENT NUMBER
C69				502032
E221			SHEET	REV
Q1			1 of 10	A
CR1				